UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

NOTICE OF ALLOWANCE AND FEE(S) DUE

25889

7590

12/15/2008

COLLARD & ROE, P.C. 1077 NORTHERN BOULEVARD ROSLYN, NY 11576 EXAMINER

BAKER, DAVID S

ART UNIT PAPER NUMBER

2884 DATE MAILED: 12/15/2008

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/574.402	04/03/2006	Helmut Rieder	RIEDERHETAL3PCT	5902

TITLE OF INVENTION: INFRARED (IR)RECEIVER DEVICE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	03/16/2009

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

or <u>Fax</u> (571)-273-2885

INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where

appropriate. All further indicated unless correcte maintenance fee notificate	correspondence including bed below or directed other tions	ng the Patent, advance on nerwise in Block 1, by (a	ders and notification of a) specifying a new corre	maintenance fees will spondence address; a	ll be mailed to the current and/or (b) indicating a sep	t correspondence address as arate "FEE ADDRESS" for
		ock 1 for any change of address)	Fee par	e(s) Transmittal. This pers. Each additional	certificate cannot be used.	or domestic mailings of the for any other accompanying ent or formal drawing, must
25889 COLLARD & 1077 NORTHER ROSLYN, NY 1	ROE, P.C. RN BOULEVARD	//2008		Corti	ficate of Mailing or Trans	smission g deposited with the United st class mail in an envelope above, or being facsimile date indicated below.
						(Depositor's name)
			_			(Signature)
	_					(Date)
APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR	R	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/574,402 FITLE OF INVENTION	04/03/2006 : INFRARED (IR)RECE	EIVER DEVICE	Helmut Rieder		RIEDERHETAL3PCT	5902
APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE	FEE TOTAL FEE(S) DUE	E DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	03/16/2009
EXAM	INER	ART UNIT	CLASS-SUBCLASS]		
BAKER, I	DAVID S	2884	250-339020	_		
"Fee Address" ind: PTO/SB/47; Rev 03-0 Number is required. 3. ASSIGNEE NAME A	ondence address (or Cha 3/122) attached. ication (or "Fee Address 2 or more recent) attach	Indication form and Use of a Customer A TO BE PRINTED ON	• •	o 3 registered patent ively, le firm (having as a ragent) and the names orneys or agents. If no printed.	member a 2 s of up to o name is 3	document has been filed for
4a. The following fee(s): Issue Fee Publication Fee (N	iate assignee category or	4Ł permitted)	b. Payment of Fee(s): (Ple A check is enclosed. Payment by credit ca The Director is hereb	Individual Corase first reapply any	poration or other private gr previously paid issue fee is attached. the required fee(s), any december 1	
a. Applicant claim	tus (from status indicates s SMALL ENTITY statu	is. See 37 CFR 1.27.	☐ b. Applicant is no lor	nger claiming SMALI	ENTITY status. See 37 C	EFR 1.27(g)(2).
NOTE: The Issue Fee and interest as shown by the i	d Publication Fee (if requeeords of the United Sta	uired) will not be accepted tes Patent and Trademark	d from anyone other than Office.	the applicant; a regist	ered attorney or agent; or t	he assignee or other party in
Authorized Signature				Date		
Typed or printed name				Registration No	·	
an application. Confident submitting the completed his form and/or suggesti	tiality is governed by 35 I application form to the ons for reducing this bu. Tirginia 22313-1450. DC	U.S.C. 122 and 37 CFR USPTO. Time will vary rden, should be sent to the	1.14. This collection is estable depending upon the indice Chief Information Office	stimated to take 12 m vidual case. Any con ser. U.S. Patent and T	inutes to complete, includi nments on the amount of ti rademark Office, U.S. Der	d by the USPTO to process) ng gathering, preparing, and me you require to complete partment of Commerce, P.O. for Patents, P.O. Box 1450,

PTOL-85 (Rev. 08/07) Approved for use through 08/31/2010.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450

P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE FIRST NAMED INVENTOR		ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/574,402	04/03/2006	Helmut Rieder	RIEDERHETAL3PCT 5902		
25889 7	590 12/15/2008		EXAM	INER	
COLLARD & R	OE, P.C.	BAKER, DAVID S			
1077 NORTHERN BOULEVARD			ART UNIT	PAPER NUMBER	
ROSLYN, NY 11:	576		2884		
			DATE MAILED: 12/15/2008		

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 286 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 286 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

	T & 11 /1 N1					
	Application No.	Applicant(s)				
Nation of Allowability	10/574,402	RIEDER ET AL.				
Notice of Allowability	Examiner	Art Unit				
	DAVID S. BAKER	2884				
The MAILING DATE of this communication appeal All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT R	(OR REMAINS) CLOSED in or other appropriate comm IGHTS. This application is	n this application. If not included unication will be mailed in due cours				
1. This communication is responsive to 28 June 2008.						
2. ☑ The allowed claim(s) is/are <u>1-4,6 and 8-12</u> .						
 3. Acknowledgment is made of a claim for foreign priority ur a) All b) Some* c) None of the: 1. Certified copies of the priority documents have 2. Certified copies of the priority documents have 3. Copies of the certified copies of the priority do 	e been received. e been received in Applicatio	on No	rom the			
International Bureau (PCT Rule 17.2(a)).						
* Certified copies not received:						
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONN THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		e a reply complying with the requirer	nents			
 A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give 			E OF			
5. CORRECTED DRAWINGS (as "replacement sheets") mus	st be submitted.					
(a) ☐ including changes required by the Notice of Draftspers	son's Patent Drawing Review	w (PTO-948) attached				
1) ☐ hereto or 2) ☐ to Paper No./Mail Date						
(b) ☐ including changes required by the attached Examiner's Paper No./Mail Date	(b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of					
Identifying indicia such as the application number (see 37 CFR 1 each sheet. Replacement sheet(s) should be labeled as such in t) of			
6. DEPOSIT OF and/or INFORMATION about the depo attached Examiner's comment regarding REQUIREMENT			he			
Attachment(s) 1. ☑ Notice of References Cited (PTO-892)	5. ☐ Notice of Ir	formal Patent Application				
2. Notice of Draftperson's Patent Drawing Review (PTO-948)		ummary (PTO-413),				
3. Information Disclosure Statements (PTO/SB/08),	Paper No. 7.	/Mail Date Amendment/Comment				
Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit	8. 🛛 Examiner's	Statement of Reasons for Allowand	е			
of Biological Material	9.	→				
/David S Baker/						
Examiner, Art Unit 2884						
	1					

Application/Control Number: 10/574,402 Page 2

Art Unit: 2884

DETAILED ACTION

Response to Amendment

1. The amendment filed on 28 July 2008 has been accepted and entered.

Allowable Subject Matter

- 2. Claims 1-4, 6, and 8-12 are allowed.
- 3. The following is an examiner's statement of reasons for allowance:

Regarding claim 1, the prior art of record does not disclose or reasonably suggest, along with the other claimed limitations, an infrared detector array comprising, namely: the combination of a processor circuit having a maximum detector circuit, Wherein each IR detector element for selecting the maximum output signal has at least on consecutive diode, the diodes being interconnected by their sides facing away from the IR detector element. The prior art of record, such as Scott, discloses the use of readout diodes connected via their cathodes to a common node. However, not every IR detector element has its own readout diode; the diodes are shared along detector element rows or columns. Essentially, each detector element must have an output diode, which is not the detector element itself, wherein these output diodes are connected via their cathodes to a common node as part of a maximum detector circuit.

Regarding claims 2-4, 6, and 8-10, the claims are found allowable due to their dependence upon an already allowed claim and lacking any technical errors.

Regarding claim 11, the prior art of record does not disclose or reasonably suggest, along with the other claimed limitations, an infrared detector array comprising, namely: the combination of a processor circuit having a maximum detector circuit,

Art Unit: 2884

Wherein each IR detector element for selecting the maximum output signal has at least on consecutive diode, the diodes being interconnected by their sides facing away from the IR detector element. The prior art of record, such as Scott, discloses the use of readout diodes connected via their cathodes to a common node. However, not every IR detector element has its own readout diode; the diodes are shared along detector element rows or columns. Essentially, each detector element must have an output diode, which is not the detector element itself, wherein these output diodes are connected via their cathodes to a common node as part of a maximum detector circuit. For further explanations, please also refer to the previous office action filed 28 March 2008, specifically the section of the Allowable Subject Matter's Reasons for Allowance of previous claim 5 as the instant claim is an independent version of previous claim 5.

Regarding claim 12, the prior art of record does not disclose or reasonably suggest, along with the other claimed limitations, an infrared detector array comprising, namely: the combination of a processor circuit having a maximum detector circuit, Wherein each IR detector element for selecting the maximum output signal has at least on consecutive diode, the diodes being interconnected by their sides facing away from the IR detector element. The prior art of record, such as Scott, discloses the use of readout diodes connected via their cathodes to a common node. However, not every IR detector element has its own readout diode; the diodes are shared along detector element rows or columns. Essentially, each detector element must have an output diode, which is not the detector element itself, wherein these output diodes are connected via their cathodes to a common node as part of a maximum detector circuit. For further explanations, please

also refer to: Sections 1.3 and 1.4 of the Reasoned Statement for PCT/AT2004/000331; and the previous office action filed 28 March 2008, specifically the section of the Allowable Subject Matter's Reasons for Allowance of previous claim 7 as the instant claim is an independent version of previous claim 7.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Response to Arguments

4. Applicant's arguments, see pages 10-11 of the amendment, filed 28 July 2008, with respect to claims 1-4, 6, and 8-12 have been fully considered and are persuasive. The rejections of the claims have been withdrawn. Please see the above section, Allowable Subject Matter, for further explanation.

Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure:

US 2008/0251721 A1 – Ueno discloses an infrared detector array where each detector element is a diode.

US 6,707,497 B1 – Pantigny discloses a pixels biasing circuit having a diode for each pixel connected via resistive ladder network.

US 5,731,621 A – Kosai discloses a multiband detector element having the diode detector elements connected via their anodes to each other.

Application/Control Number: 10/574,402 Page 5

Art Unit: 2884

US 3,495,086 A – Scott discloses the use of readout diodes connected via their cathodes to a common node but as common rows or columns, not individually per detector element.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to DAVID S. BAKER whose telephone number is (571)272-6003. The examiner can normally be reached on MTWRF 10:30am-7:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, David P. Porta can be reached on (571) 272-2444. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/David S Baker/
Examiner, Art Unit 2884
/David P. Porta/
Supervisory Patent Examiner, Art Unit 2884